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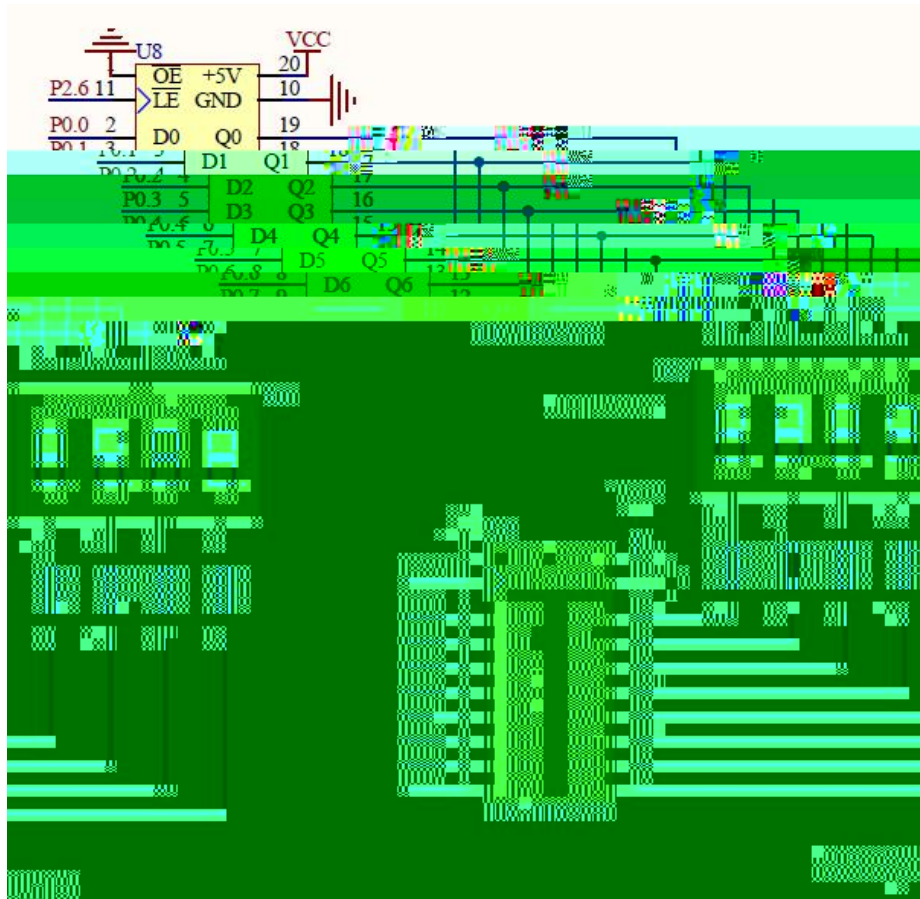








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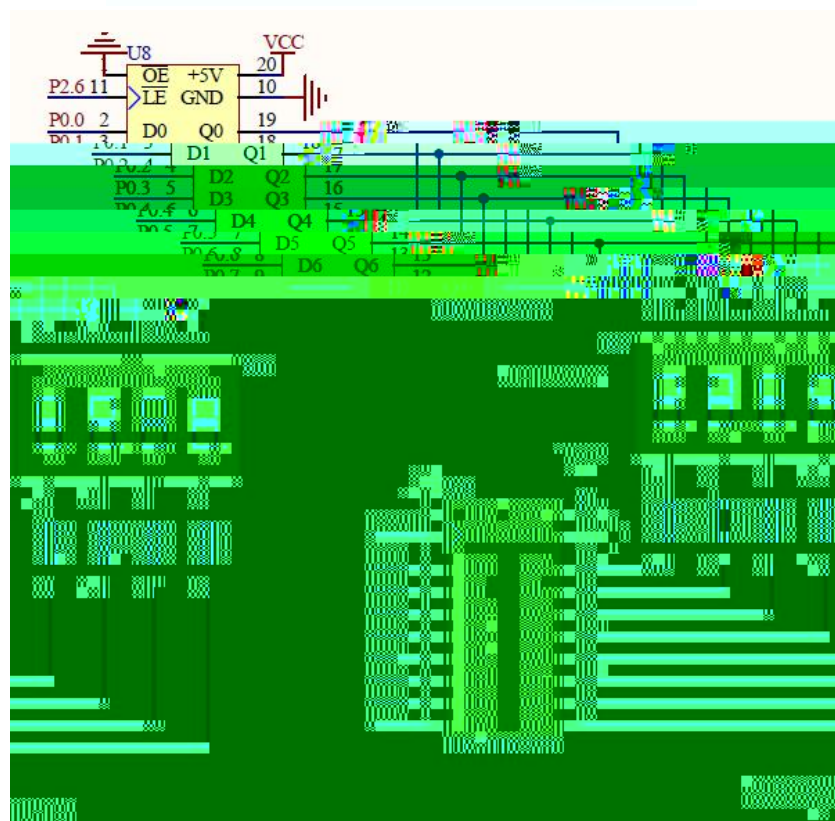
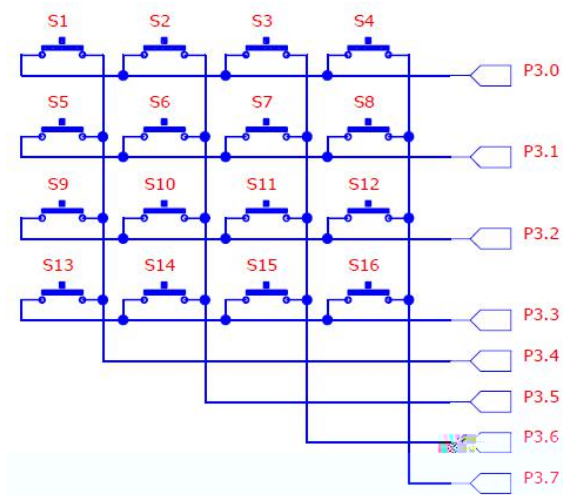
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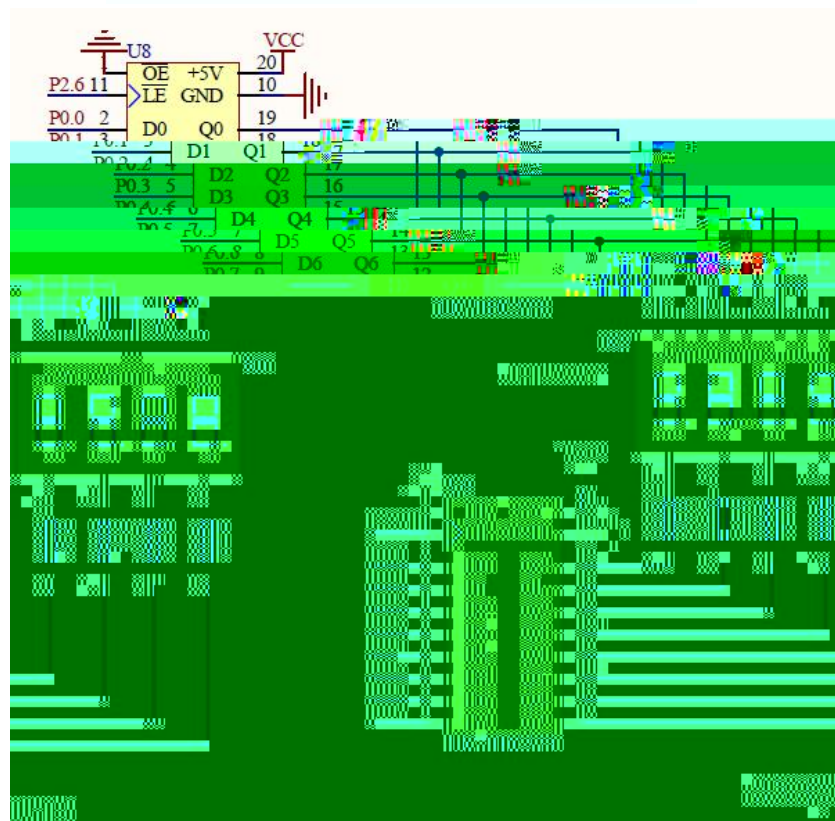
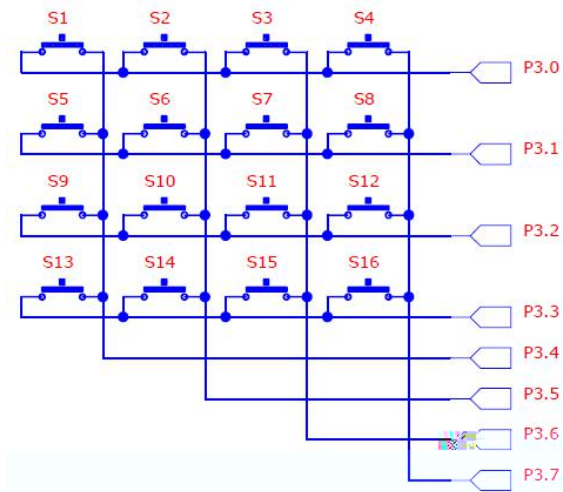
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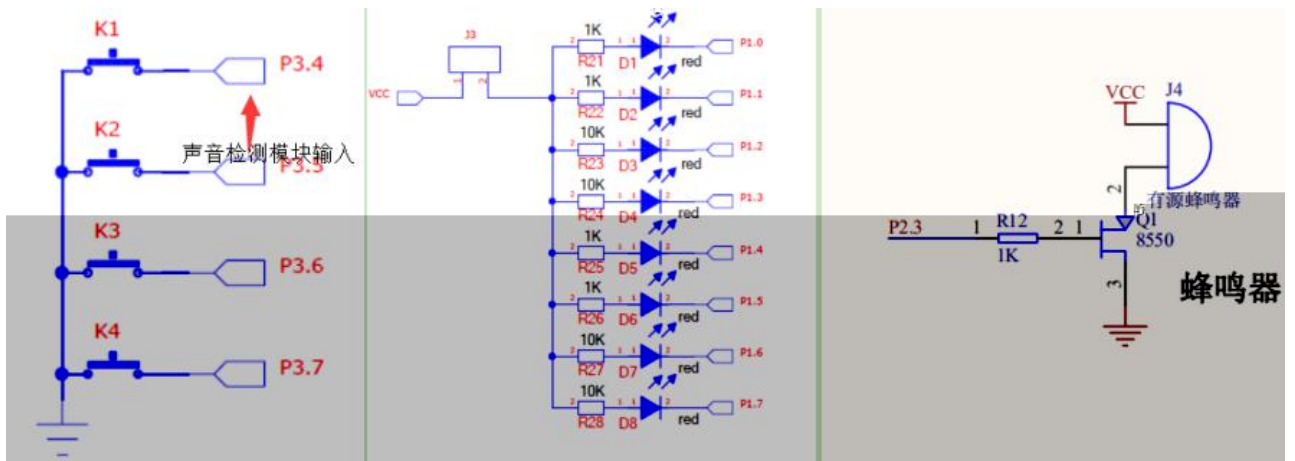
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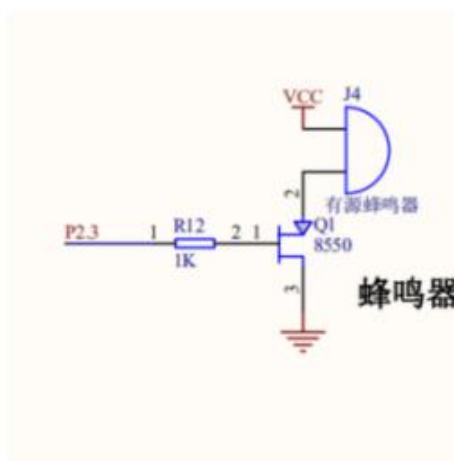
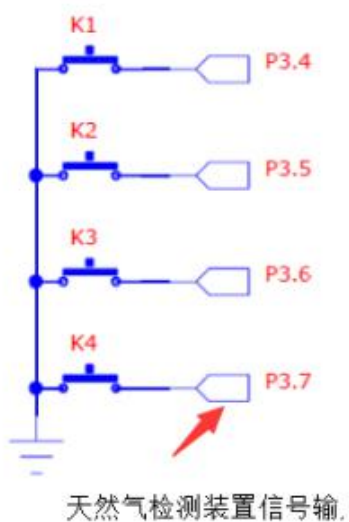
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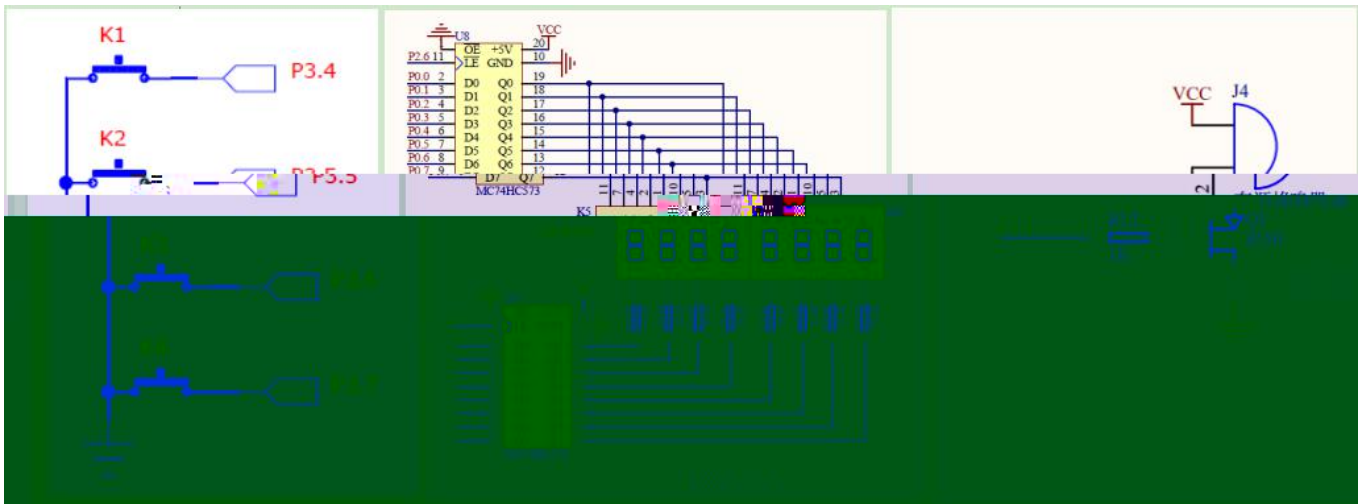
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1	VBAT	VDD	64
2	PC13/ANT1	VSS	63
3	PC14/OSC32	PB9/TIM4_CH4/SDIO_D5	62
4	PC15/OSC32	PB8/TIM4_CH3/SDIO_D4	61
5	PD0/OSCIN	OSC_IN	60
6	PD1/OSCOUT	OSC_OUT	59
7	NRES	DD7/IO1204/LDA/TIM4_CH2	58
8	PC0/ADC10	PB6/I2C1_SCL/TIM4_CH1	57
9	PC1/ADC11	PB5/I2C1_SMBA/SPI3_MOSI/I2S3_SD	56
10	PC2/ADC12	PB4/JNTRST/SPI3_MISO	55
11	PC3/ADC13	PB3/JTDO/SPI3_SCK/I2S3_CK	54
12	VSSA	PC2/U5_RX/TIM3_ETR/SDIO_CMD	53
13	VDDA	PC12/U5_TX/SDIO_CK	52
14	PA0/WKUP/ADC0/TIM2_CH1_ETR/TIM5_CH1/TIM8_ETR	PC11/U4_RX/SDIO_D3	51
15	PA1/ADC1/TIM2_CH3/TIM5_CH2	PC10/U4_TX/SDIO_D2	50
16	PA2/U2_TX/ADC2/TIM2_CH5/TIM5_CH3	PA15/JTDI/SPI3_NSS/I2S3_WS	49
17	PA3/U2_RX/ADC3/TIM2_CH4/TIM5_CH4	PA14/JTCK/SWCLK	48
18	VDD	VDD	47
19	PA4/AFIO_SWO/SPI2_NSS	PA13/TIM2/BKUP	46
20	PA5/AFIO_SCK/SPI2_SCK	PA12/AFIO_RESET/AFIO_LDR	45
21	PA6/AFIO_MOSI/SPI2_MOSI	PA11/AFIO_MISO/SDIO_CMD4	44
22	PA7/AFIO_MISO/SPI2_MISO	PA10/AFIO_RX/TIM1_CH2	43
23	PA8/AFIO_CS/SPI2_CS	PA9/AFIO_TX/TIM1_CH4	42
24	PA9/AFIO_NSS/SPI2_NSS	PA8/AFIO_ETR/TIM1_CH3	41
25	PA10/AFIO_SCK/SPI2_SCK	PA7/AFIO_CS/SPI2_CS	40
26	PA11/AFIO_MOSI/SPI2_MOSI	PA6/AFIO_MISO/SPI2_MISO	39
27	PA12/AFIO_MISO/SPI2_MISO	PA5/AFIO_RX/TIM1_CH2	38
28	PA13/AFIO_TX/TIM1_CH4	PA4/AFIO_TX/TIM1_CH4	37
29	PA14/AFIO_RX/TIM1_CH2	PA3/AFIO_TX/TIM1_CH4	36
30	PA15/AFIO_CS/SPI2_CS	PA2/AFIO_TX/TIM1_CH2	35
31	VDD	PA1/AFIO_CS/SPI2_CS	34
32	VSS	PA0/AFIO_CS/SPI2_CS	33
33	VDD	PA0/AFIO_CS/SPI2_CS	32
34	VSS	PA0/AFIO_CS/SPI2_CS	31
35	VDD	PA0/AFIO_CS/SPI2_CS	30
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37	VDD	PA0/AFIO_CS/SPI2_CS	28
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62	VSS	PA0/AFIO_CS/SPI2_CS	3
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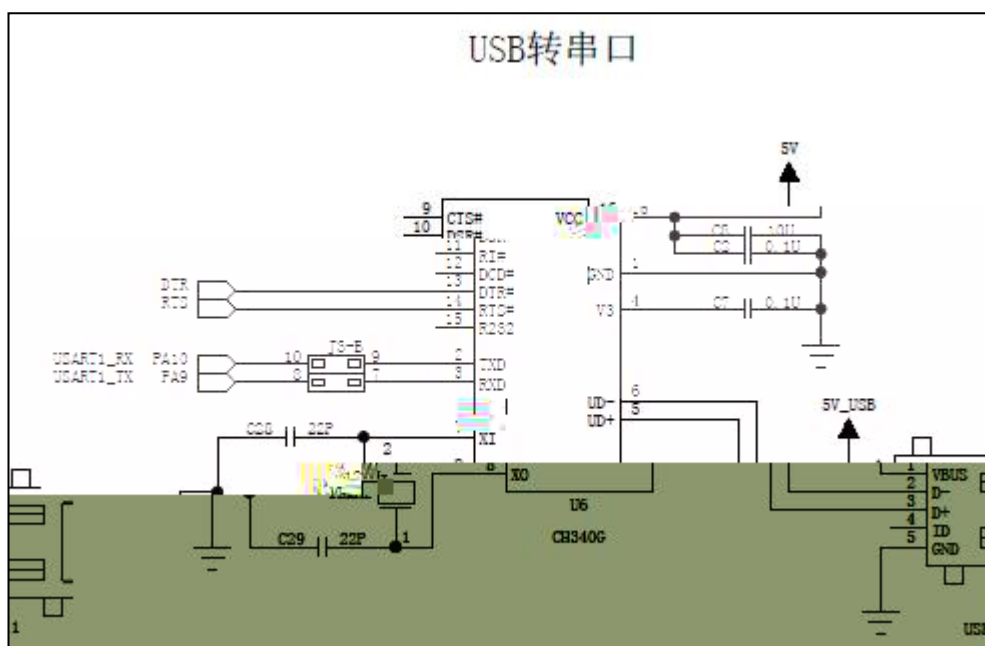
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2	PC13/ANT1	VSS	63
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4	PC15/OSC32	PB8/TIM4_CH3/SDIO_D4	61
5	PD0/OSCIN		
6	PD1/OSCOUT		
7	NFS		
8	PC0 ADC10		
9	PC1 ADC11		
10	PC2 ADC12		
11	PC3 ADC13		
12	VSSA		
13	VDDA		
14	PA0 WKUP ADC0/TIM2_CH1 ETR/TIM5_CH1/TIM8_ETR		
15	PA1 ADC11/M2_CH2/M5_CH2		
16	PA2 J2 TX ADC2/TIM2_CH3/TIM5_CH3		
17	PA3 J2 RX ADC3/TIM2_CH4/TIM5_CH4		
18	VSS		
19	VDD		
20	PA4 J2 TX ADC4/TIM2_CH5/TIM5_CH5		
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202	PA186 J2 TX ADC186/TIM2_CH187/TIM5_CH187		
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206	PA190 J2 TX ADC190/TIM2_CH191/TIM5_CH191		
207	PA191 J2 RX ADC191/TIM2_CH192/TIM5_CH192		
208	PA192 J2 TX ADC192/TIM2_CH193/TIM5_CH193		
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216	PA200 J2 TX ADC200/TIM2_CH201/TIM5_CH201		
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218	PA202 J2 TX ADC202/TIM2_CH203/TIM5_CH203		
219	PA203 J2 RX ADC203/TIM2_CH204/TIM5_CH204		
220	PA204 J2 TX ADC204/TIM2_CH205/TIM5_CH205		
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222	PA206 J2 TX ADC206/TIM2_CH207/TIM5_CH207		
223	PA207 J2 RX ADC207/TIM2_CH208/TIM5_CH208		
224	PA208 J2 TX ADC208/TIM2_CH209/TIM5_CH209		
225	PA209 J2 RX ADC209/TIM2_CH210/TIM5_CH210		
226	PA210 J2 TX ADC210/TIM2_CH211/TIM5_CH211		
227	PA211 J2 RX ADC211/TIM2_CH212/TIM5_CH212		
228	PA212 J2 TX ADC212/TIM2_CH213/TIM5_CH213		
229	PA213 J2 RX ADC213/TIM2_CH214/TIM5_CH214		
230	PA214 J2 TX ADC214/TIM2_CH215/TIM5_CH215		
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235	PA219 J2 RX ADC219/TIM2_CH220/TIM5_CH220		
236	PA220 J2 TX ADC220/TIM2_CH221/TIM5_CH221		
237	PA221 J2 RX ADC221/TIM2_CH222/TIM5_CH222		
238	PA222 J2 TX ADC222/TIM2_CH223/TIM5_CH223		
239	PA223 J2 RX ADC223/TIM2_CH224/TIM5_CH224		
240	PA224 J2 TX ADC224/TIM2_CH225/TIM5_CH225		
241	PA225 J2 RX ADC225/TIM2_CH226/TIM5_CH226		
242	PA226 J2 TX ADC226/TIM2_CH227/TIM5_CH227		
243	PA227 J2 RX ADC227/TIM2_CH228/TIM5_CH228		
244	PA228 J2 TX ADC228/TIM2_CH229/TIM5_CH229		
245	PA229 J2 RX ADC229/TIM2_CH230/TIM5_CH230		
246	PA230 J2 TX ADC230/TIM2_CH231/TIM5_CH231		
247	PA231 J2 RX ADC231/TIM2_CH232/TIM5_CH232		
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254	PA238 J2 TX ADC238/TIM2_CH239/TIM5_CH239		
255	PA239 J2 RX ADC239/TIM2_CH240/TIM5_CH240		
256	PA240 J2 TX ADC240/TIM2_CH241/TIM5_CH241		
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258	PA242 J2 TX		



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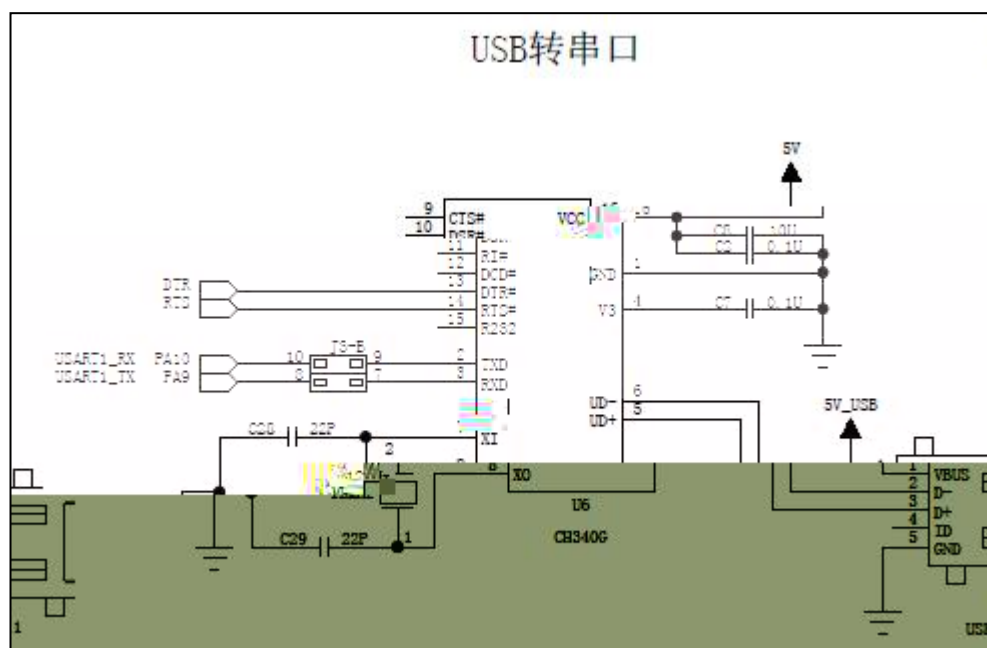


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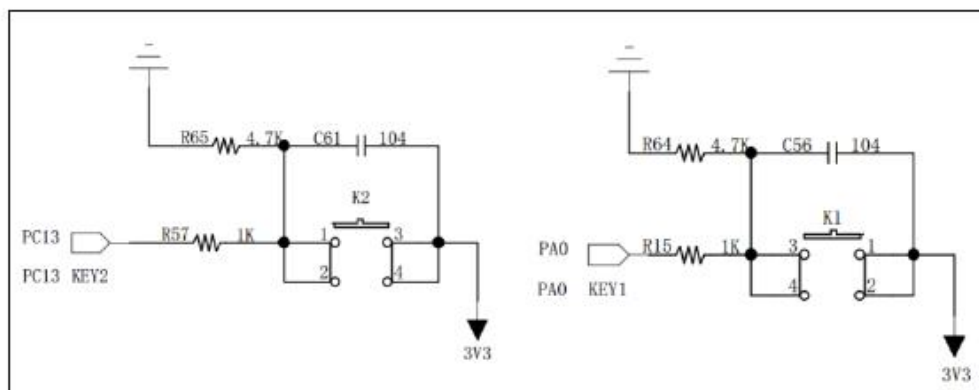


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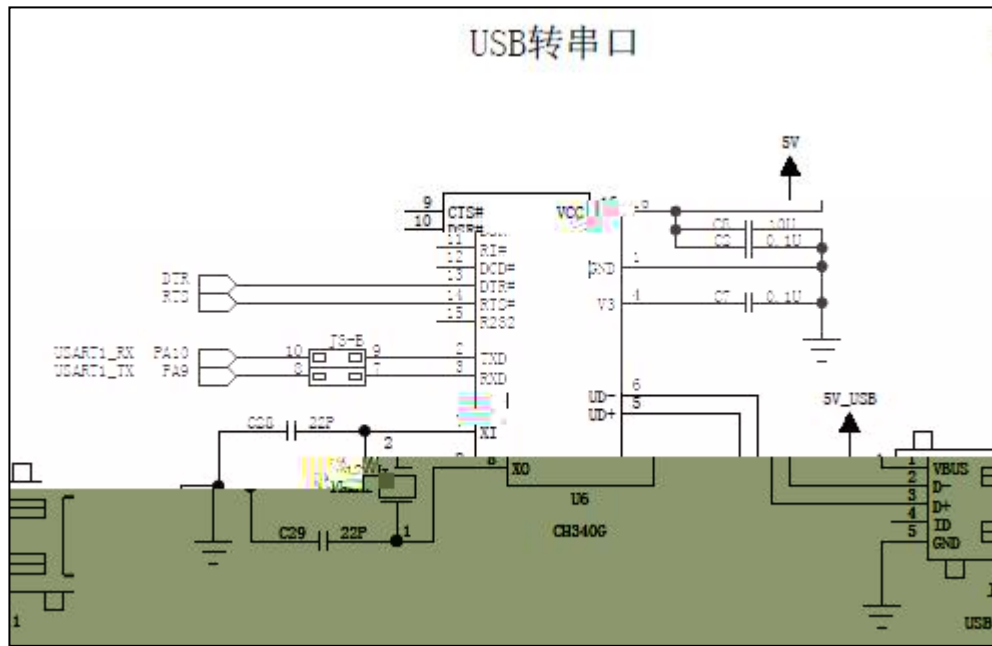


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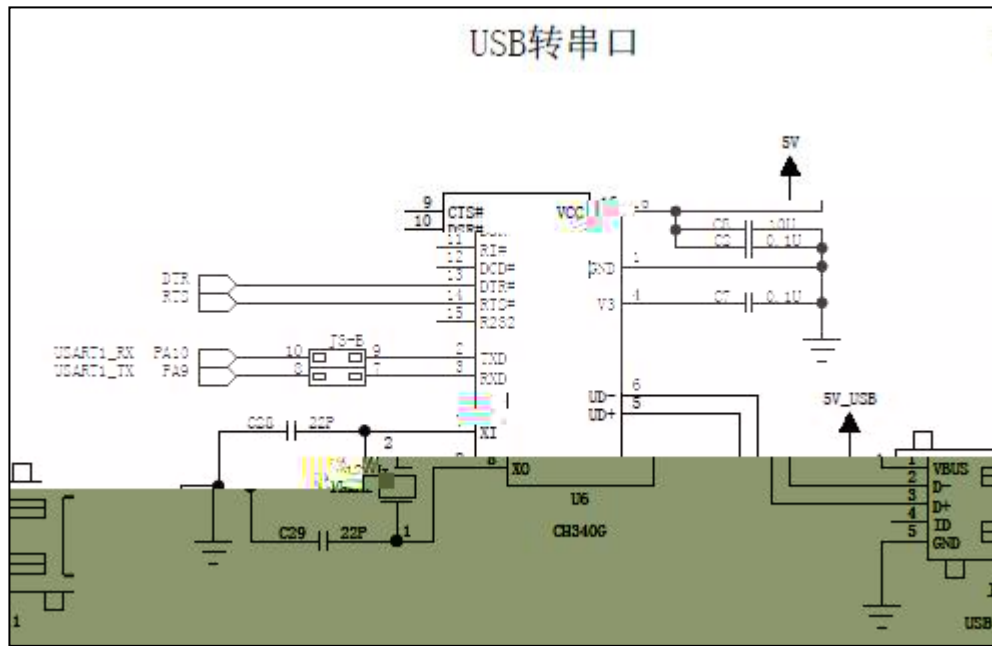


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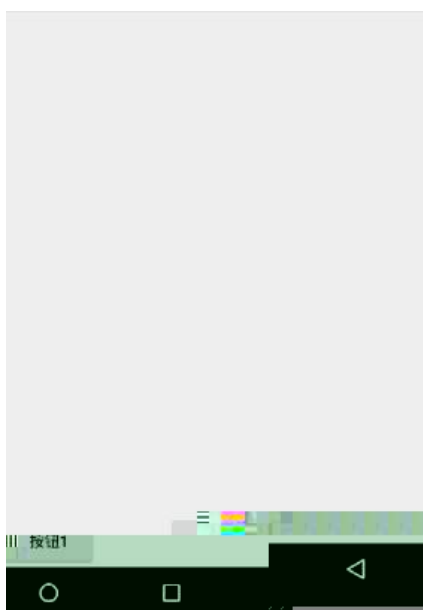
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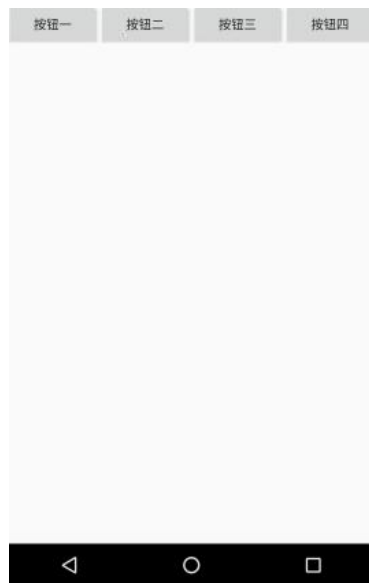
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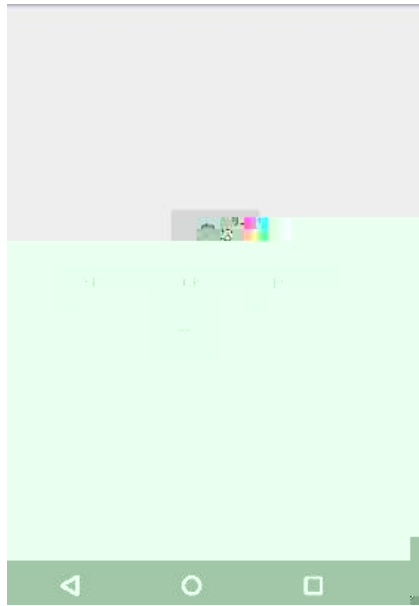


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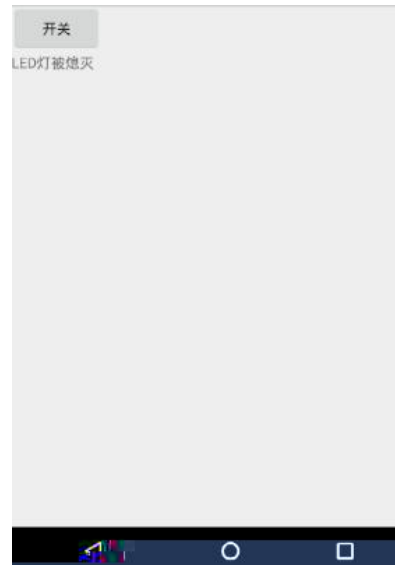
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